



Attorney Docket No. 0756-7251

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Shunpei YAMAZAKI
Serial No. 10/759,297
Filed: January 20, 2004
For: METHOD OF MANUFACTURING A
SEMICONDUCTOR DEVICE

) Group Art Unit: 2812
) Examiner:
) CERTIFICATE OF MAILING
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Adelle M. Stumper

REQUEST FOR CORRECTED FILING RECEIPT

Honorable Commissioner of Patents
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Sir:

The Filing Receipt received in the above-identified application has errors thereon
due to Patent Office error as shown in red on the attached copy.

In view of the above, it is requested that a Corrected Filing Receipt be issued.

Respectfully submitted,


Eric J. Robinson
Reg. No. 38,285

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/759,297	01/20/2004	2812	770	0756-7251	10	16	2

CONFIRMATION NO. 7182

31780
 ERIC ROBINSON
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APR 26 2004

FILING RECEIPT



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41
 4-26-04

Date Mailed: 04/22/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s) Shunpei
Shunpei Yamazaki, Setagaya, JAPAN;

Assignment For Published Patent Application

Semiconductor Energy Laboratory Co., Ltd., Atsugi-shi, JAPAN;

Domestic Priority data as claimed by applicant

This application is a DIV of 10/124,344 04/18/2002
 which is a DIV of 09/337,333 06/21/1999 PAT 6,380,046

Foreign Applications

JAPAN 10-174482 06/22/1998

If Required, Foreign Filing License Granted: 04/21/2004

Projected Publication Date: 07/29/2004

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing a semiconductor device

Preliminary Class

438

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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